STANDARDS AND AES53-2018 (revision of AES53-2006) INFORMATION DOCUMENTS



AES standard for digital audio — Digital input-output interfacing — Sample-accurate timing in AES47

Users of this standard are encouraged to determine if they are using the latest printing incorporating all current amendments and editorial corrections. Information on the latest status, edition, and printing of a standard can be found at: http://www.aes.org/standards

AUDIO ENGINEERING SOCIETY, INC.

60 East 42nd Street, New York, New York 10165, US.

This is a preview. Click here to purchase the full publication.



The AES Standards Committee is the organization responsible for the standards program of the Audio Engineering Society. It publishes technical standards, information documents and technical reports. Working groups and task groups with a fully international membership are engaged in writing standards covering fields that include topics of specific relevance to professional audio. Membership of any AES standards working group is open to all individuals who are materially and directly affected by the documents that may be issued under the scope of that working group.

Complete information, including working group scopes and project status is available at <u>http://www.aes.org/standards</u>. Enquiries may be addressed to <u>standards@aes.org</u>

The AES Standards Committee is supported in part by those listed below who, as Standards Sustainers, make significant financial contribution to its operation.



AES standard for digital audio -Digital input-output interfacing -Sample-accurate timing in AES47

Published by **Audio Engineering Society, Inc.** Copyright © 2006, 2018 by the Audio Engineering Society

Abstract

This document specifies how the timing markers specified in AES47 may be used to associate an absolute timestamp with individual audio samples. AES47 specifies a format for the transmission of digital audio over asynchronous transfer mode (ATM) networks. A recommendation is made to refer these timestamps to the SMPTE epoch which in turn provides a reference to UTC and GPS time.

An AES standard implies a consensus of those directly and materially affected by its scope and provisions and is intended as a guide to aid the manufacturer, the consumer, and the general public. The existence of an AES standard does not in any respect preclude anyone, whether or not he or she has approved the document, from manufacturing, marketing, purchasing, or using products, processes, or procedures not in agreement with the standard. Prior to approval, all parties were provided opportunities to comment or object to any provision. Attention is drawn to the possibility that some of the elements of this AES standard or information document may be the subject of patent rights. AES shall not be held responsible for identifying any or all such patents. Approval does not assume any liability to any patent owner, nor does it assume any obligation whatever to parties adopting the standards document. This document is subject to periodic review and users are cautioned to obtain the latest edition. Recipients of this document are invited to submit, with their comments, notification of any relevant patent rights of which they are aware and to provide supporting documentation.



2018-08-02 printing

Contents

0 Introduction	4
0.1 General 0.2 Patents	4 4
1 Scope	4
2 Normative references	4
3 Definitions and abbreviations	5
4 Numbering of samples	5
5 Numbering of cells	5
6 Sequence numbers and blocks	6
Annex A: (Informative) Background information	7
A.1 Choice of origin of sample numbering	7 7 7



2018-08-02 printing